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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/665,034 09/19/2000
which is a CON of 09/153,630 09/15/1998 PAT 6,153,829

yes *m*

** FOREIGN APPLICATIONS *****

None *m*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/03/2003

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	AZ	DRAWING 3	CLAIMS 24	CLAIMS 3
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	<i>m</i>	Initials			

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TITLE

Methods of split cavity wall plating for an integrated circuit package

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